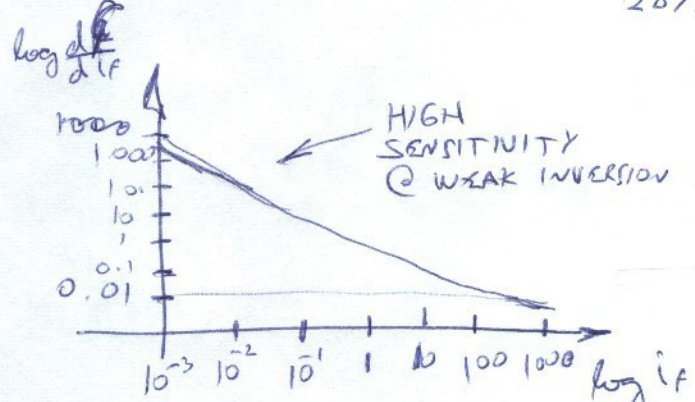


$$\Delta i_f = \frac{\Delta V_p}{V_T \left(\frac{\partial f}{\partial i_f} \right)} = \left[\frac{0.0036}{V_T}, \frac{0.099}{V_T} \right]$$

$$\Delta I_D = I_S \Delta i_f \approx \left[0.14 \mu A, 3.81 \mu A \right]$$

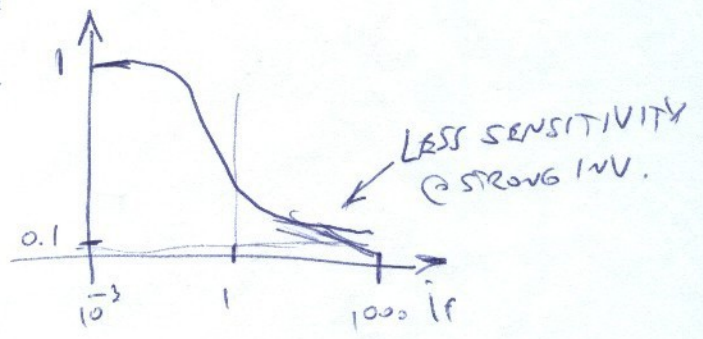
28% 7.6%

← LESS EFFECT NEAR STRONG INVERSION

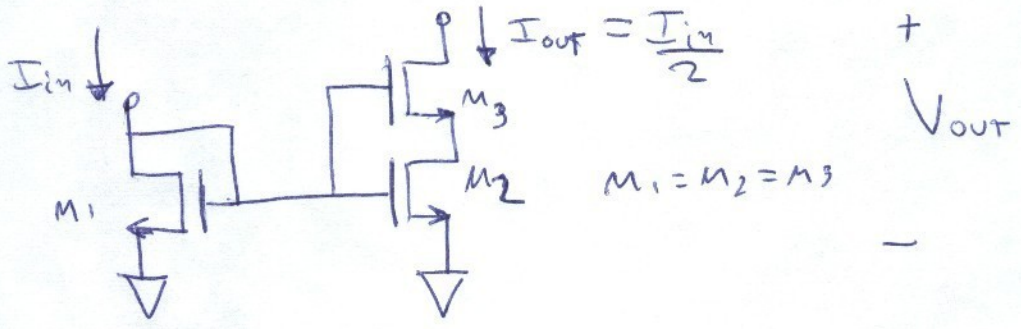
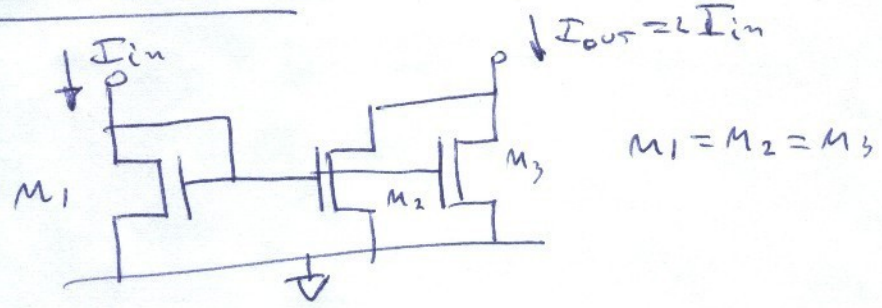


∴ MATCHING MORE DIFFICULT IN WEAK INVERSION.

$$\frac{\Delta i_f}{i_f} = \frac{\Delta V_p}{V_T i_f \left(\frac{\partial f}{\partial i_f} \right)} \propto \frac{1}{i_f \left(\frac{\partial f}{\partial i_f} \right)}$$

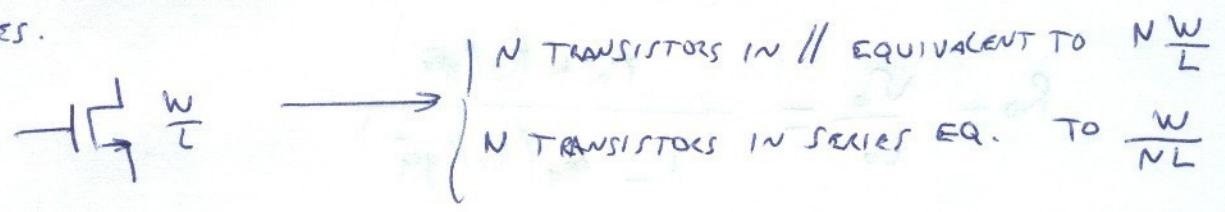


CURRENT MIRROR GAIN

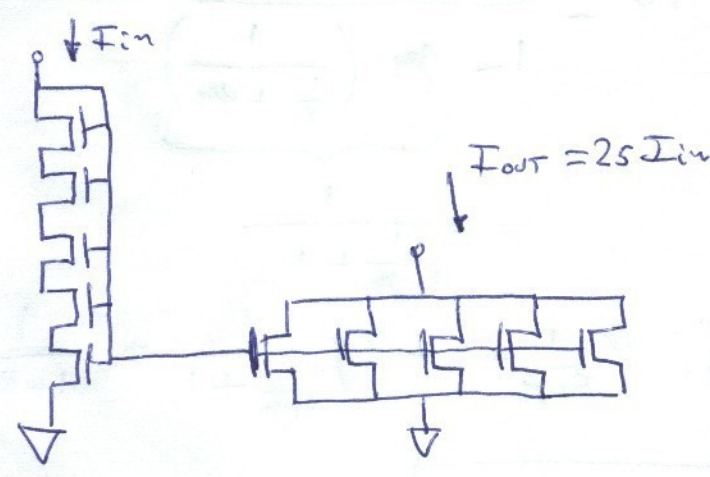


- $M_1 = M_2$ AND $V_{G1} = V_{G2} \Rightarrow i_{f1} = i_{f2}$
- $i_{r2} = i_{f3}$ SINCE $V_{D2} = V_{S3}$ AND $M_2 = M_3$ (M_2 TRIODE)
- ASSUMING V_{OUT} IS HIGH ENOUGH, $i_{r3} \approx 0$ (M_3 ACTIVE REGION)
- $\therefore i_{f3} = i_{f2} - i_{r2} = i_{f2} - i_{f3}$
- $\therefore i_{f3} = \frac{i_{f2}}{2} = \frac{i_{f1}}{2} \Rightarrow I_{OUT} = \frac{I_{in}}{2}$

THE SAME WORKS FOR ANY NUMBER OF TRANSISTORS IN SERIES.

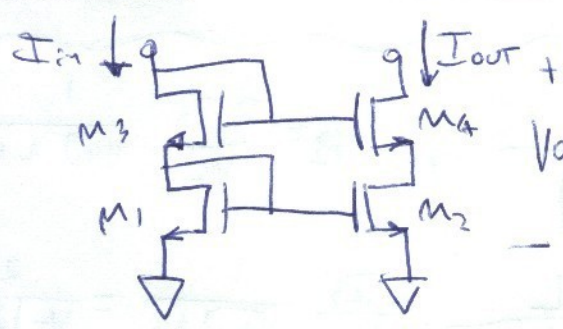


EXAMPLE: PROBLEM 5.4



IMPORTANT: TRANSISTORS ARE ALL EQUAL \rightarrow GOOD MATCHING.

CASCODE CURRENT MIRRORS: TO GET HIGH OUTPUT RESISTANCE



V_G , M_1 AND M_2 OPERATE ALMOST @ EXACT SAME OPERATING POINT
 \therefore SAME DRAW CURRENTS

• ASSUME ALL TRANSISTORS ACTIVE: $i_{F1,2,3,4} \ll I_{F1,2,3,4}$ (11)

• FROM TOPOLOGY IT FOLLOWS $I_{F1} = I_{F2} = I_{F3} = I_{F4} = I_F \rightarrow$ SOLVE FOR V_{G1}

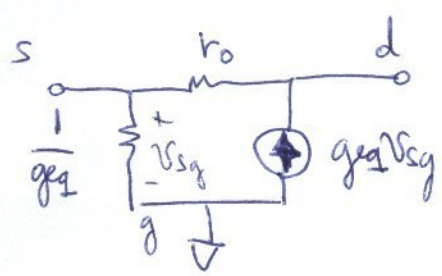
$$V_{DS4} > V_{DSAT} = V_T (\sqrt{I_F} + 3)$$

$$\therefore V_{OMIN} = V_{G1} + V_T (\sqrt{I_F} + 3)$$

• OUTPUT RESISTANCE \rightarrow USE SMALL-SIGNAL MODEL. (PAGE 14)

$$g_m = \frac{qms}{n}$$

$$g_{mb} = g_{ms} \left(\frac{n-1}{n} \right)$$



$$g_{eq} = g_{mq} + g_{mb} = 1$$

$$g_{eq} = g_{ms} \left(\frac{1}{n} + \frac{n-1}{n} \right)$$

$$= g_{ms}$$

$$R_o \approx r_{o2} (g_{ms4} r_{o4}) \rightarrow r_{o2} \text{ AMPLIFIED BY } g_{ms4} r_{o4}$$

$$r_o = \frac{L}{I_D \left| \frac{\partial X_D}{\partial V_{DS}} \right|} \approx \frac{L}{I_S \mu_f \left| \frac{\partial X_D}{\partial V_{DS}} \right|}$$

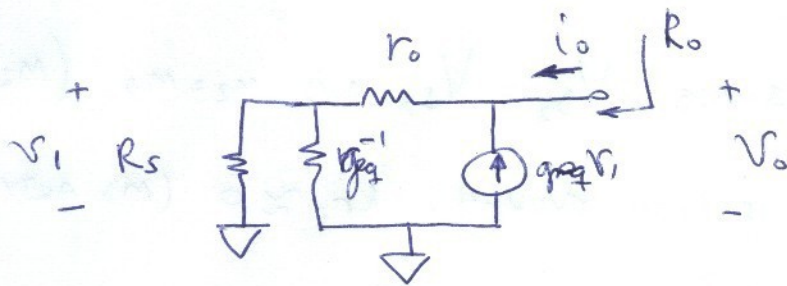
$$g_{ms} = \frac{2I_S}{V_T} (\sqrt{I_F} - 1)$$

$$R_o \approx \frac{L}{I_S \mu_f \left| \frac{\partial X_D}{\partial V_{DS}} \right|} \cdot \frac{2I_S}{V_T} (\sqrt{I_F} - 1) \frac{L}{I_S \mu_f \left| \frac{\partial X_D}{\partial V_{DS}} \right|}$$

$$R_o \approx \frac{2L^2}{I_S \left| \frac{\partial X_D}{\partial V_{DS}} \right|^2} \cdot \frac{(\sqrt{I_F} - 1)}{\mu_f^2} \leftarrow \text{CAN GET A BETTER EXPRESSION}$$

(EXAMPLE 5.5)

(11)



$$v_1 = v_o \frac{(R_s \parallel g_m^{-1})}{r_o + (R_s \parallel g_m^{-1})}$$

$$i_o = \frac{v_o}{r_o + (R_s \parallel g_m^{-1})} \left[1 - g_m (R_s \parallel g_m^{-1}) \right]$$

$$R_o = \frac{v_o}{i_o} = \frac{r_o + (R_s \parallel g_m^{-1})}{1 - g_m (R_s \parallel g_m^{-1})}$$

$$r_o \gg g_m^{-1} \Rightarrow R_o \approx \frac{r_o}{1 - g_m \left(\frac{1}{\frac{1}{R_s} + g_m} \right)}$$

$$= \frac{1}{\frac{1}{R_s g_m} + 1}$$

Denominator: $\left(\frac{1}{R_s g_m} + 1 - 1 \right) \frac{1}{\frac{1}{R_s g_m} + 1} = \frac{1}{1 + R_s g_m} \approx \frac{1}{R_s g_m}$

$$\therefore R_o \approx (r_o g_m) R_s$$

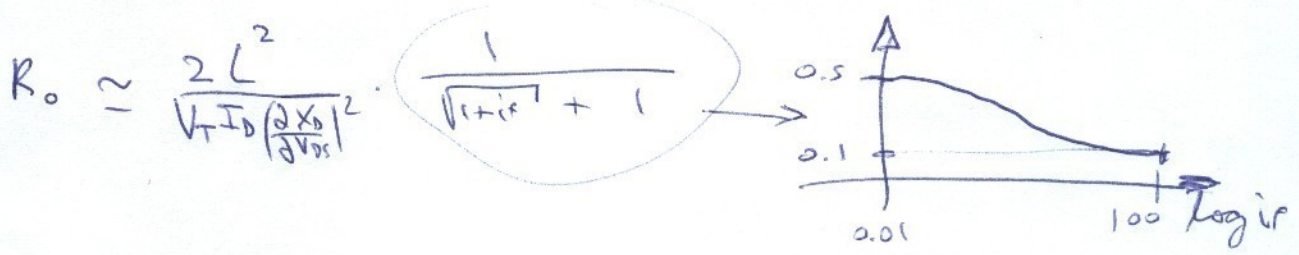
$$r_o = \frac{L}{I_D \left| \frac{dX_0}{dV_{GS}} \right|}, \quad g_m = \frac{2 I_D}{V_T} \frac{1}{\sqrt{1+\eta} + 1}$$

$$\frac{r_o}{g_m^{-1}} = r_o g_m \gg 1 \rightarrow \frac{L}{I_D \left| \frac{dX_0}{dV_{GS}} \right|} \cdot \frac{2 I_D}{V_T} \frac{1}{\sqrt{1+\eta} + 1} = \frac{2L}{V_T \left| \frac{dX_0}{dV_{GS}} \right| (\sqrt{1+\eta} + 1)} > 1$$

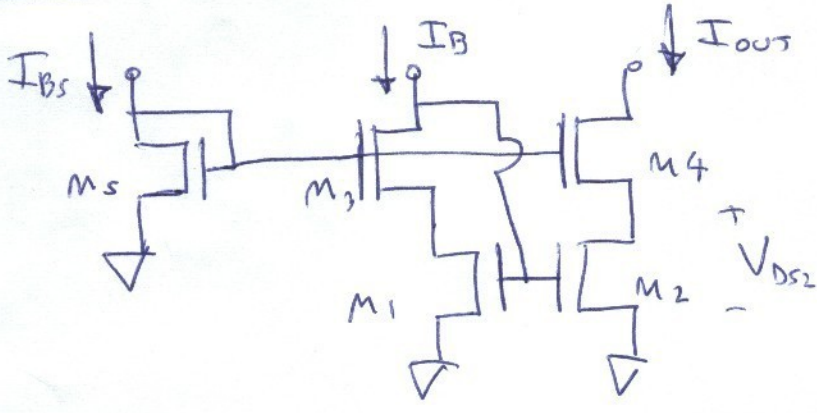
$$g_{m5} = \frac{2I_D}{V_T} \frac{1}{\sqrt{1+i_f^2} + 1} \quad (\text{FOR SATURATION ONLY})$$

$$R_o \approx \frac{2 I_D}{V_T} \frac{1}{\sqrt{1+i_f^2} + 1} \cdot \frac{L^2}{I_D \left| \frac{\partial X_D}{\partial V_{DS}} \right|^2}$$

• ASSUME $I_D = \text{CONSTANT}$ AND WE ONLY CAN CHANGE INVOLSION LEVEL



HIGH-SWING CASCODE MIRROR



$$V_{P5} = V_{P3}$$

$$V_{P5} = V_T \left[\sqrt{1+i_{f5}^2} - 2 + \ln(\sqrt{1+i_{f5}^2} - 1) \right]$$

$$= V_{P4} = V_{S4} + V_T \mathcal{F}(i_{f4})$$

$$V_{S4} = V_{DS_{SAT2}} + \Delta V \Rightarrow \text{CHOOSE } \Delta V \approx V_T$$

$$V_{P5} = V_{DS_{SAT2}} + V_T (1 + \mathcal{F}(i_{f4}))$$

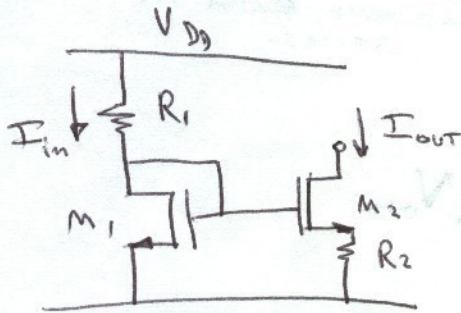
$$\text{BUT } V_{DS_{SAT2}} = V_T (\sqrt{1+i_{f2}^2} + 3)$$

$$V_{P5} = V_T \left[\sqrt{1+i_{f2}^2} + 3 + 1 + \mathcal{F}(i_{f4}) \right] \rightarrow \begin{matrix} \text{IF } M4 \equiv M2 \text{ THEN} \\ i_{f4} = i_{f2} \end{matrix}$$

GIVEN $i_{f2}, i_{f4} \Rightarrow \text{SOLVE FOR } i_{f5}$

LOW-CURRENT BIASING

• WIDLAR CURRENT SOURCE. → Low OUTPUT CURRENT WITH NOT SO LARGE RESISTOR.



$$V_{GS1} - V_{GS2} - I_{OUT} R_2 = 0$$

$$V_{GS} = V_{OV} + V_T$$

$$\therefore I_{OUT} R_2 + V_{OV2} - V_{OV1} = 0$$

$$\sqrt{I_{OUT}} = \frac{-\sqrt{\frac{2}{k'(W/L)_2}} + \sqrt{\frac{2}{k'(W/L)_2} + 4R_2 V_{OV1}}}{2R_2}$$

EXAMPLE: $I_{in} = 10 \mu A$, $R_2 = 4 k\Omega$, $k' = 200 \frac{\mu A}{V^2}$, $(\frac{W}{L})_1 = (\frac{W}{L})_2 = 2.5$

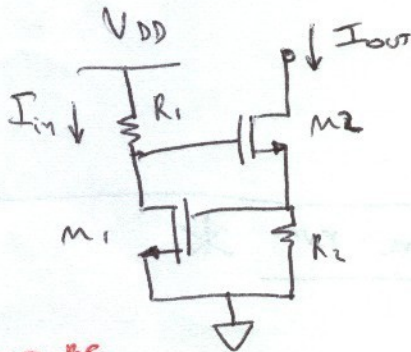
$$\sqrt{I_{OUT}} = 5 \sqrt{\mu A} \Rightarrow I_{OUT} = 25 \mu A$$

$$V_{OV2} = 0.1 V > 2 V_T = 78 mV$$

(STRONG INVERSION)
(NOT REALLY)

• CURRENT SOURCE USING A V_{th} STANDARD

EXAMPLE (*)
PAGE 364



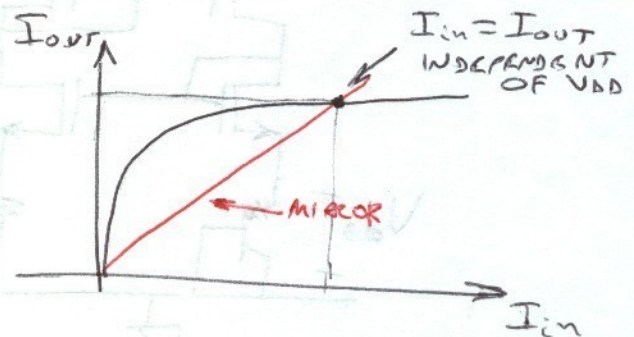
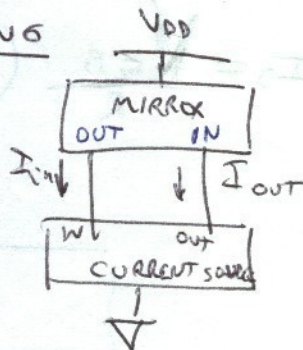
$$I_{OUT} = \frac{V_{GS1}}{R_2} = \frac{V_T + \sqrt{\frac{2 I_{in}}{k' (\frac{W}{L})_1}}}{R_2}$$

IF $V_{OV1} \ll V_T \Rightarrow$ CANNOT BE ACHIEVED WITH STRONG INVERSION.
OUTPUT CURRENT ALMOST INDEPENDENT OF I_{in} .

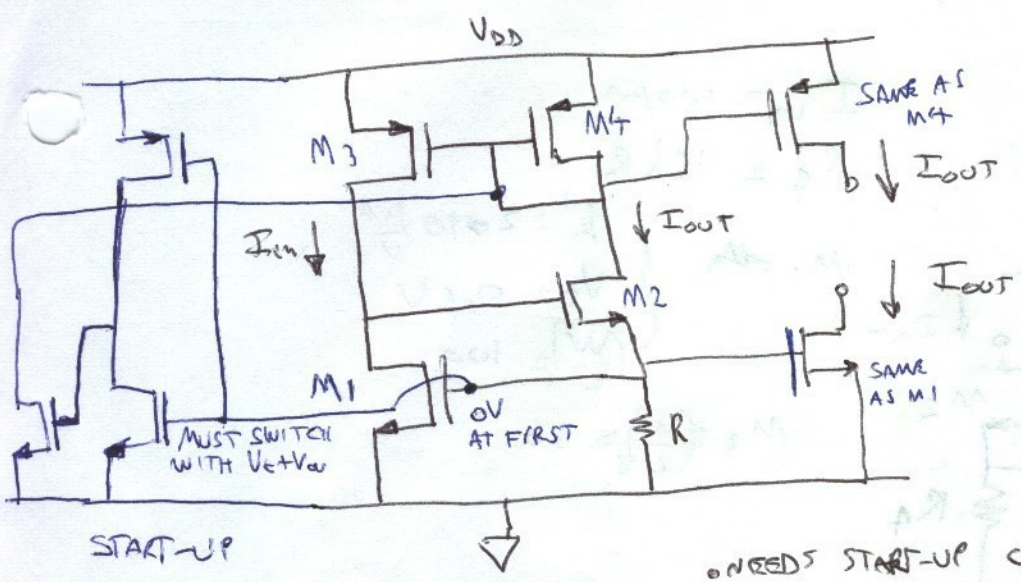
$(\frac{W}{L})$ MUST BE LARGE FOR V_{OV1} TO BE SMALL.

\therefore LESS DEPENDENCE ON V_{DD}

SELF-BIASING



SELF-BIASING V_T REFERENCE

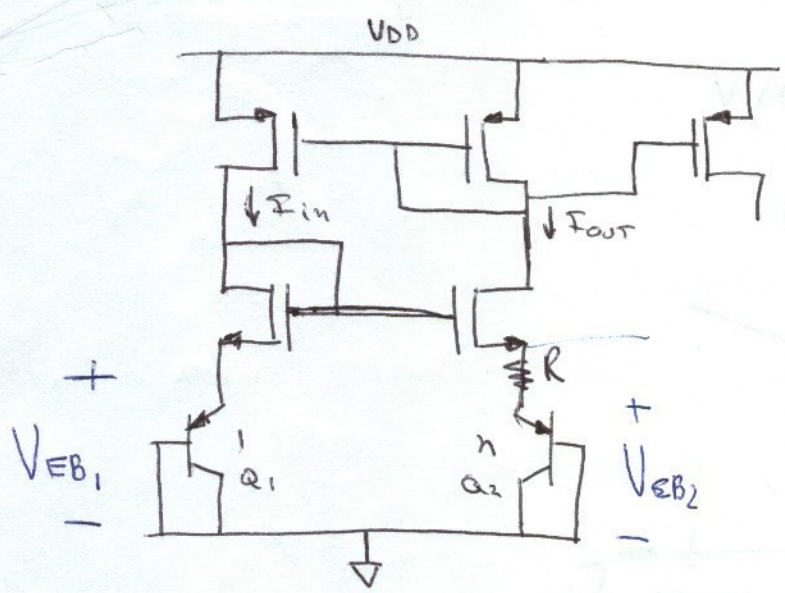


$M_3 = M_4$

(NPTAT)
 DECREASES WITH TEMP.
 SINCE V_{th} DECREASES

$$I_{out} = \frac{V_{th} + \sqrt{\frac{2I_{out}}{R} \left(\frac{W}{L}\right)_1}}{R}$$

SELF-BIASING USING V_T



$$I_{out} = \frac{V_T \ln(n)}{R}$$

$$I_s \exp\left(\frac{V_{BE1}}{V_T}\right) = n I_s \exp\left(\frac{V_{BE2}}{V_T}\right)$$

$$\frac{V_{BE1}}{V_T} = \ln(n) + \frac{V_{BE2}}{V_T}$$

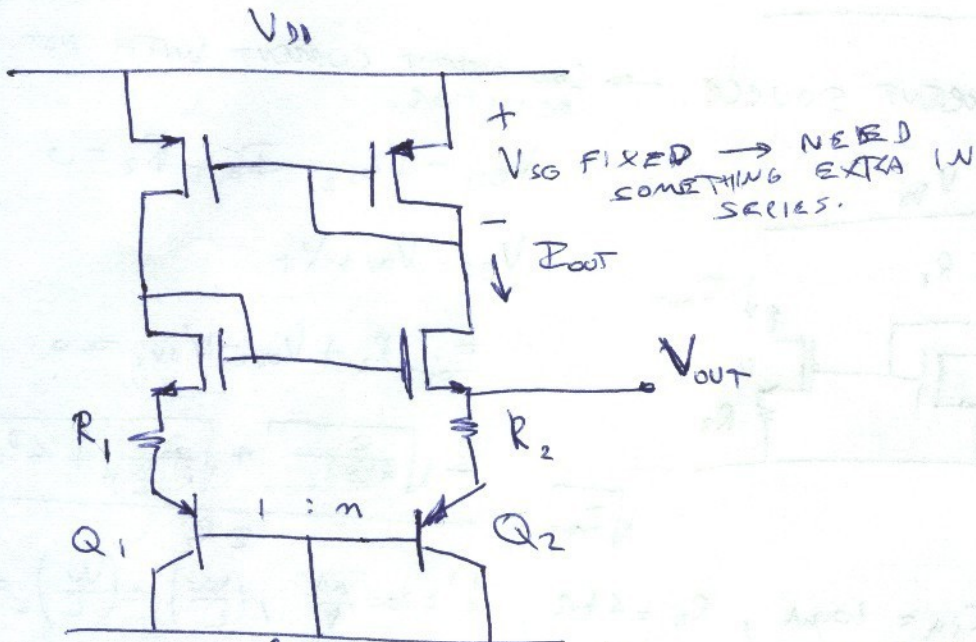
BUT $V_{BE1} = V_{BE2} + I_{out} R$

$$V_{BE2} + I_{out} R = V_T \ln(n) + V_{BE2}$$

$$I_{out} = \frac{V_T \ln(n)}{R}$$

(INCREASES WITH TEMP.)
 (PTAT)

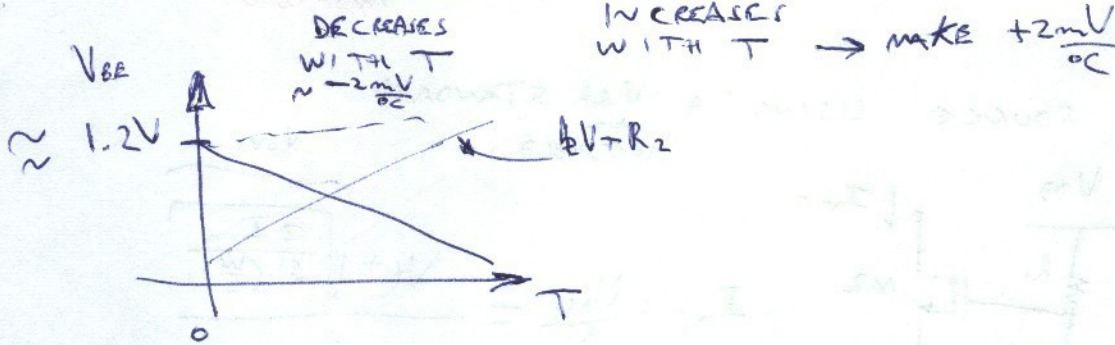
BAND GAP REFERENCE



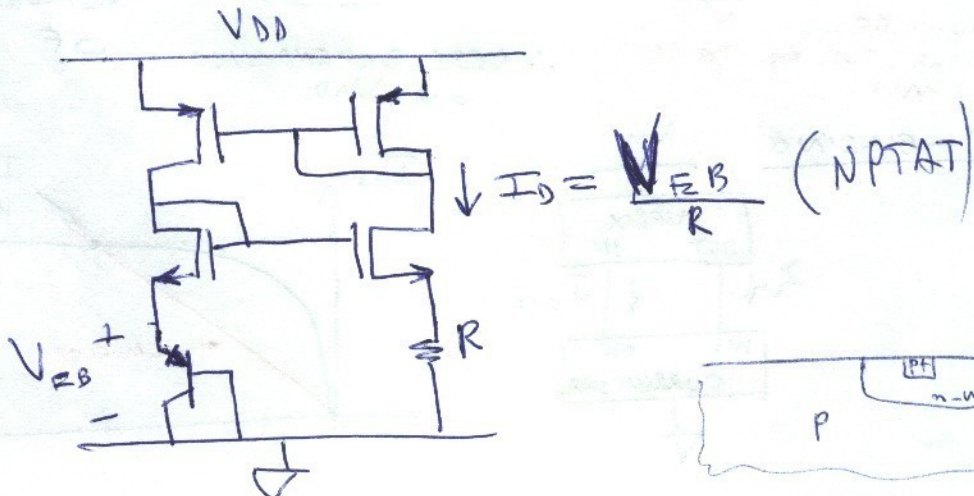
$$I_{OUT} = \frac{V_T \ln(n)}{(R_2 - R_1)} = k V_T$$

BAND GAP OF SILICON
EXTRAPOLATED @ 0K

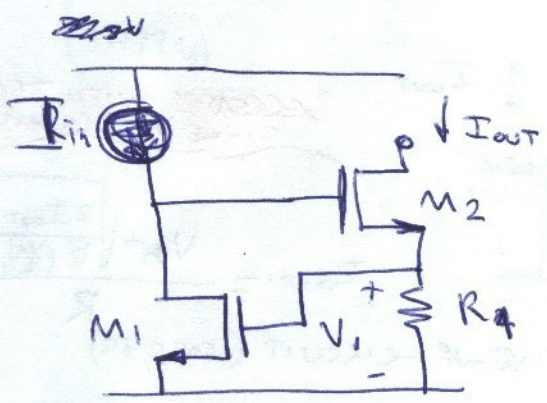
$$V_{OUT} = V_{BE2} + k V_T R_2 \approx 1.2 V$$



SELF-BIASING SOURCE USING PARASITIC LATERAL PNP *



EXAMPLE

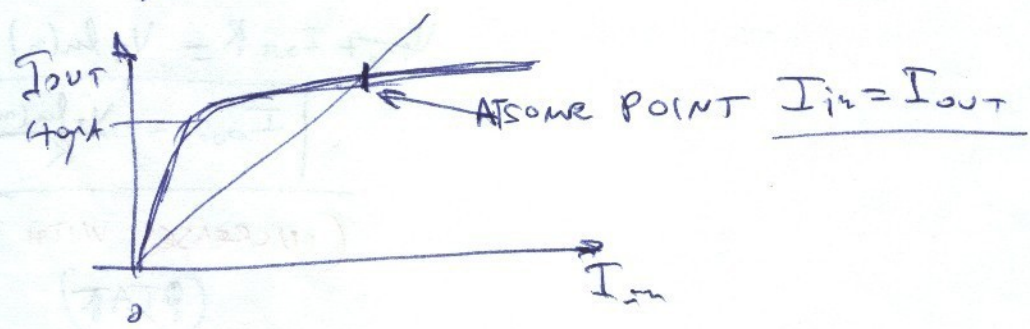
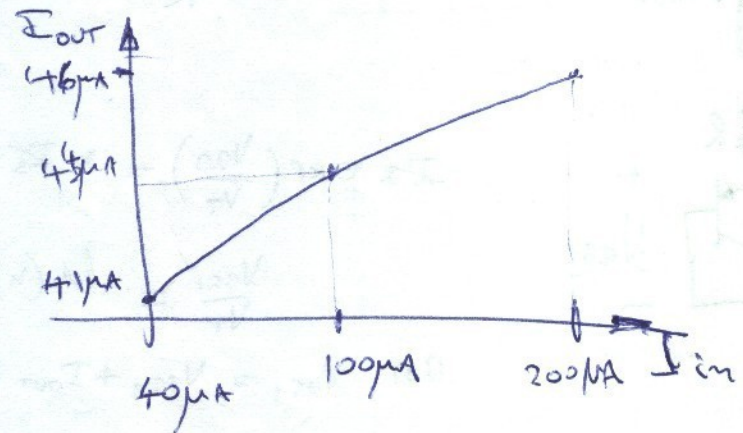


$I_{in} = 100 \mu A$
 $R_q = 16 k\Omega$
 $M_1: \left\{ \begin{array}{l} k' = 200 \frac{\mu A}{V^2} \\ V_t = 0.6 V \\ \left(\frac{W}{L}\right)_1 = 100 \end{array} \right.$
 $M_2: \left(\frac{W}{L}\right)_2 = 30$

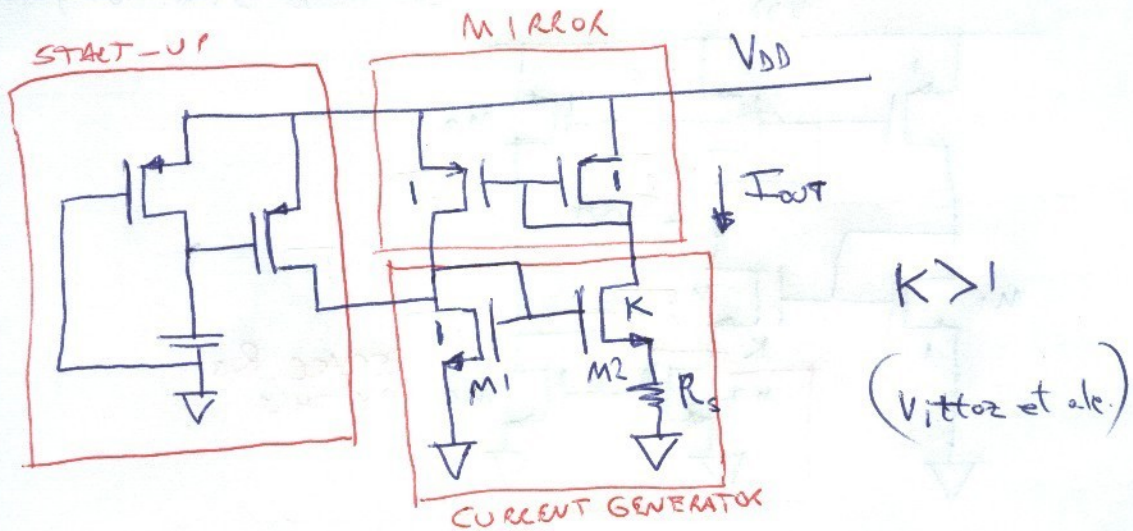
$$V_{ov,1} = \sqrt{\frac{2 I_{in}}{k' \left(\frac{W}{L}\right)_1}} \approx 97.6 mV$$

$V_1 = V_{t1} + V_{ov,1} \approx 0.698 V$

$\therefore I_{out} \approx 43.6 \mu A$



ANOTHER SELF-BIASING CURRENT SOURCE (PTAT)



M_1, M_2 WORK IN SATURATION, $I_{D1} = I_{D2}$ DUE TO MIRROR

$$I_{D1} = I_{D2} = I_{OUT}$$

$$i_{f1} = \frac{I_{OUT}}{I_S}, \quad i_{f2} = \frac{I_{OUT}}{KI_S}$$

$$\frac{V_{P1}}{V_T} = \mathcal{F}\left(\frac{I_{OUT}}{I_S}\right), \quad \frac{V_{P1} - I_{OUT} R_S}{V_T} = \mathcal{F}\left(\frac{I_{OUT}}{KI_S}\right)$$

$$\therefore \frac{I_{OUT} R_S}{V_T} = \mathcal{F}\left(\frac{I_{OUT}}{I_S}\right) - \mathcal{F}\left(\frac{I_{OUT}}{KI_S}\right)$$

FOR $i_{f1} \ll 1$ (DEEP SUBTHRESHOLD):

$$I_{OUT} \approx V_T \frac{\ln K}{R_S} \quad (\text{PTAT})$$

(ASSUMING R_S DOES NOT CHANGE MUCH WITH TEMP.)

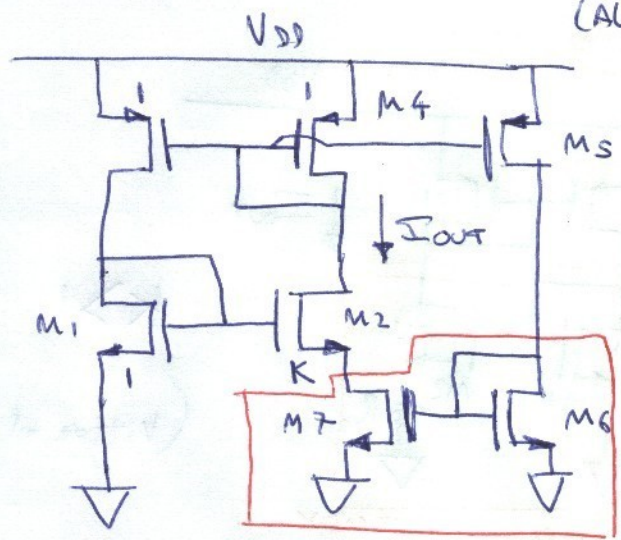
FOR $i_{f1} \gg 10$ (STRONG INVERSION)

$$I_{OUT} \approx \frac{V_T^2}{R_S^2 I_S} \left(1 - \sqrt{\frac{1}{K}}\right)^2$$

(ASSUMING R_S CONSTANT, I_{OUT} INCREASES WITH TEMP BUT NOT LINEARLY.)

OGUEY'S CURRENT SOURCE

(ALSO NEEDS START-UP)



REPLACE R_s
BY THIS

ADVANTAGE: NO RESISTOR

- ASSUME M_1, M_2 IN DEEP SUBTHRESHOLD!

$$V_{S2} \approx V_T \ln K$$

- M_7 WORKS IN TRIODE REGION
- IF M_7, M_6 OPERATE IN STRONG INVERSION,

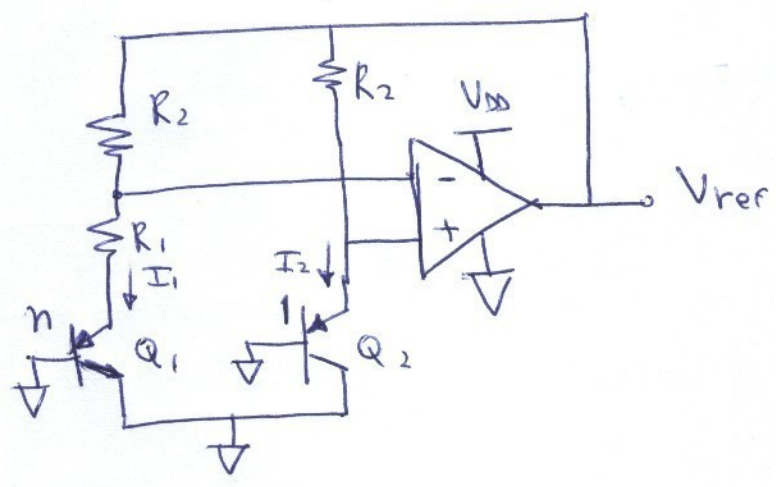
$$I_{OUT} \approx \mu_n C_{ox} n \frac{V_T^2}{2} \ln^2 K \times C_1$$

↑ CONSTANT THAT
DEPENDS ON TRANSISTOR
RATIOS ($M_4, 5, 6, 7$)

$I_{OUT} \approx I_{SQ} \Rightarrow$ CONSTANT INVERSION LEVEL
WITH TEMPERATURE.

$$I_{OUT} = I_{SQ} \ln^2 K \times C_1$$

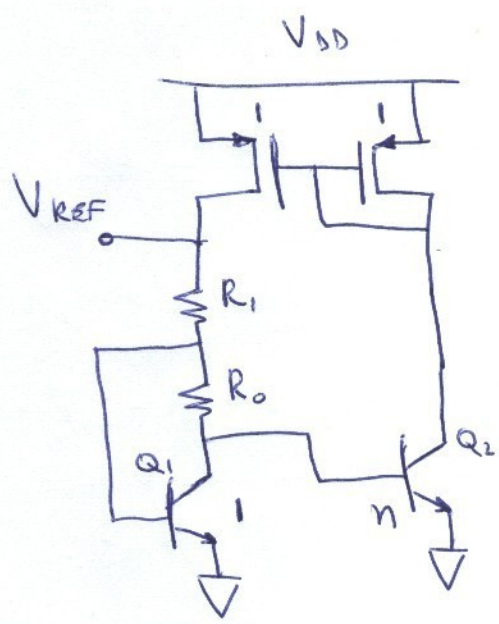
ALTERNATIVE BANDGAP REFERENCES



$I_1 = I_2$ SINCE $V^+ = V^-$

$$V_{REF} = V_{EB2} + \frac{R_2}{R_1} V_T \ln 10$$

(WORKS WITH LOWER SUPPLY VOLTAGE)



$$I_{C1} \approx \frac{V_T}{R_0} \ln K$$

$$V_{REF} \approx V_{BE1} + \frac{R_1}{R_0} V_T \ln K$$

(NEGLECTING BASE CURRENTS)

• NEEDS NPN TRANSISTORS